

11117 U.S. PTO  
09/805137



716	1
CLASS	SUBCLASS
ISSUE CLASSIFICATION	

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PATENT NUMBER

U.S. UTILITY Patent Application

SCANNED <i>043</i>	O.I.P.E. <i>Ann</i>	PATENT DATE
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APPLICATION NO. 09/805137	CONT/PRIOR D	CLASS <del>240</del> 716	SUBCLASS <del>320</del> 1	ART UNIT 218T 8825	EXAMINER <i>Thompson</i> <i>Leff</i>
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APPLICANTS

William Moore  
Sebastian Ventrone

TITLE

Microprocessor including controller for reduced power consumption and method therefor

PTO-2040  
12/99

ISSUING CLASSIFICATION

ORIGINAL		CROSS REFERENCE(S)						
CLASS	SUBCLASS	CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)					
716	1	716	2	4				
INTERNATIONAL CLASSIFICATION								

☒ Continued on Issue Slip inside File Jacket

<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b> <input checked="" type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed. <input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S. Patent No. _____ <input type="checkbox"/> The terminal _____ months of this patent have been disclaimed	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg. <i>5</i>	Figs. Drwg. <i>9</i>	Print Fig. <i>1</i>	Total Claims <i>6</i>	Print Claim for O.G. <i>1</i>
	(Assistant Examiner) _____ (Date) _____ <i>A. M. Thompson</i> <i>11-2003</i> (Primary Examiner) (Date)			<b>NOTICE OF ALLOWANCE MAILED</b>  <b>ISSUE FEE</b> Amount Due _____ Date Paid _____	
	(Legal Instruments Examiner) _____ (Date) _____			<b>ISSUE BATCH NUMBER</b>  	

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